

FIGURE 1A

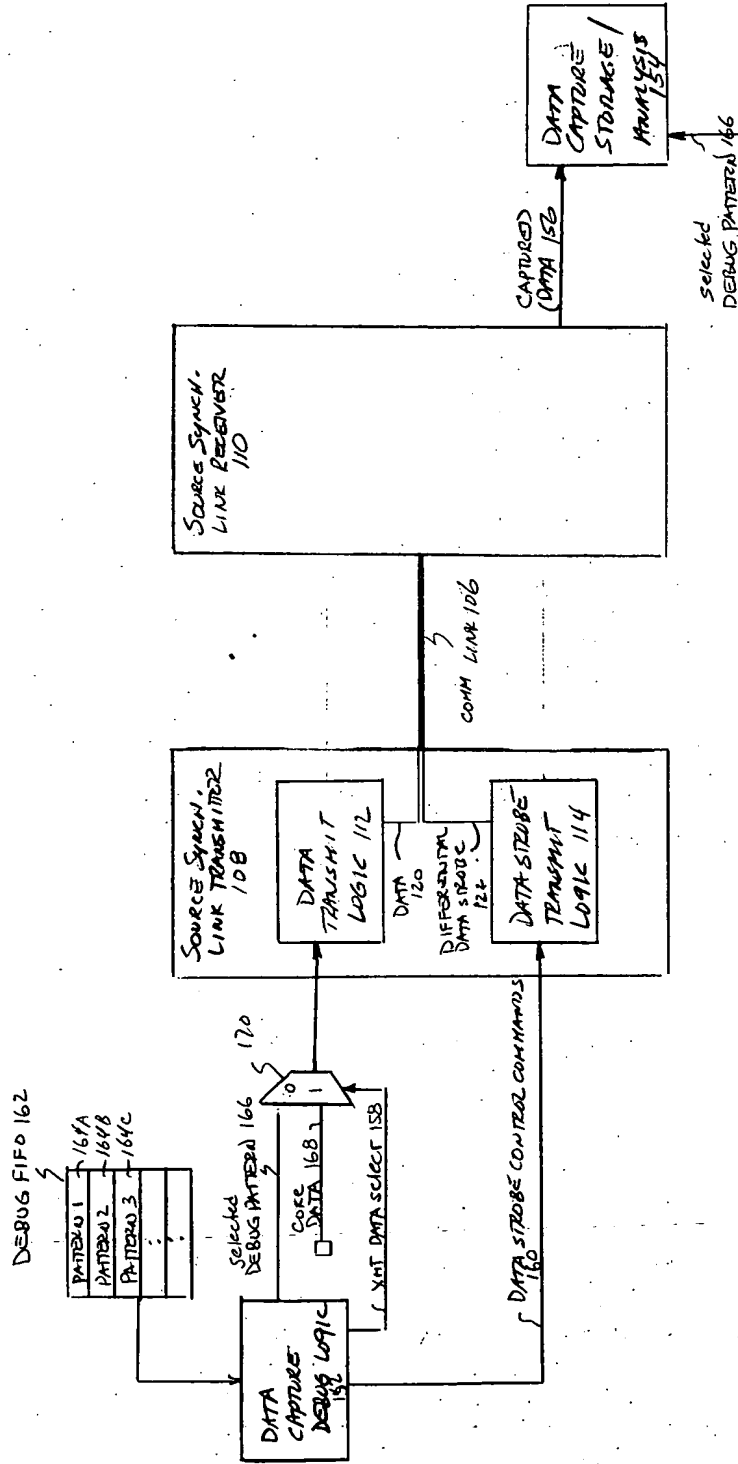


FIGURE 1B

DATA CAPTURE DEBUG
 SYSTEM 150

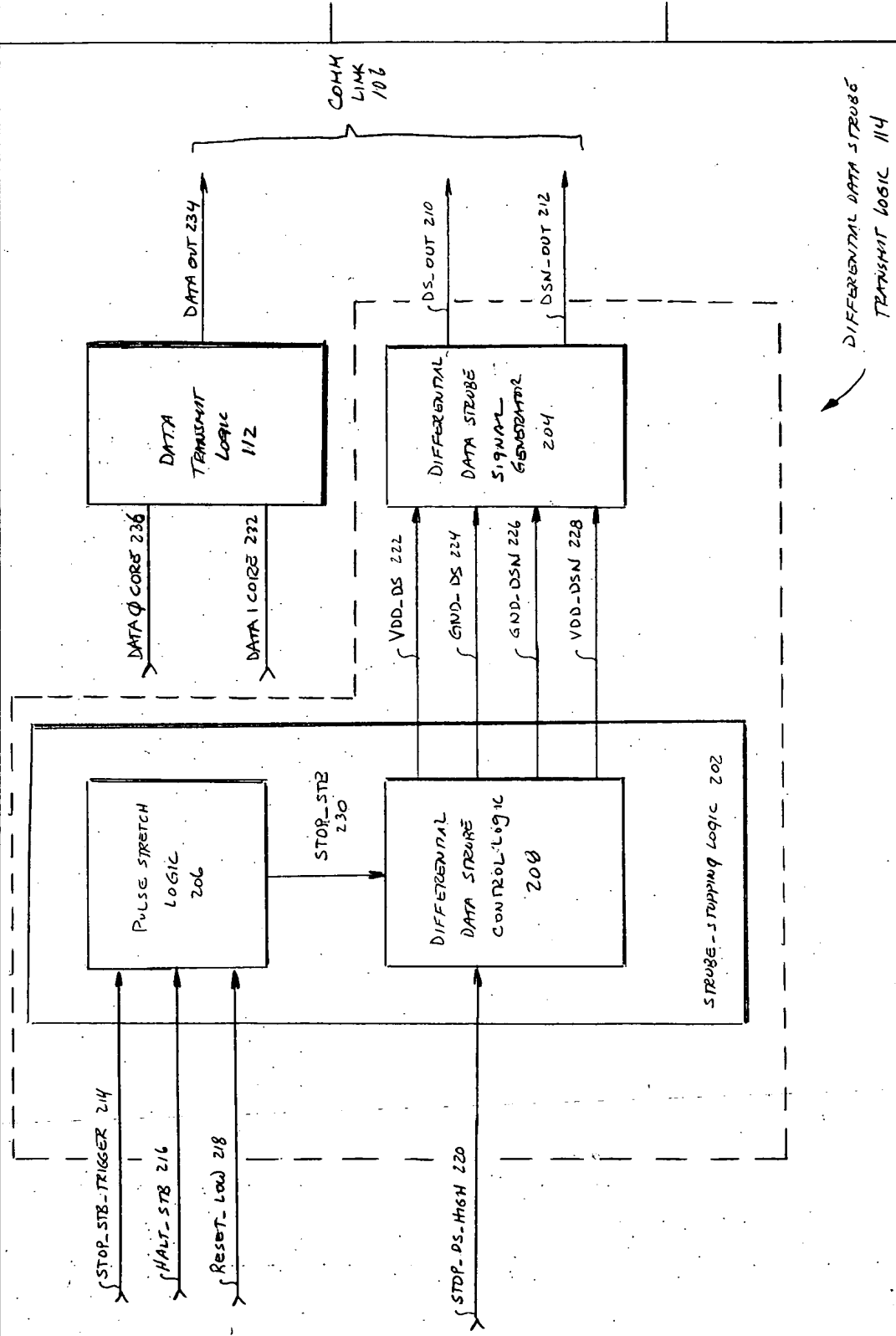


FIGURE 2A

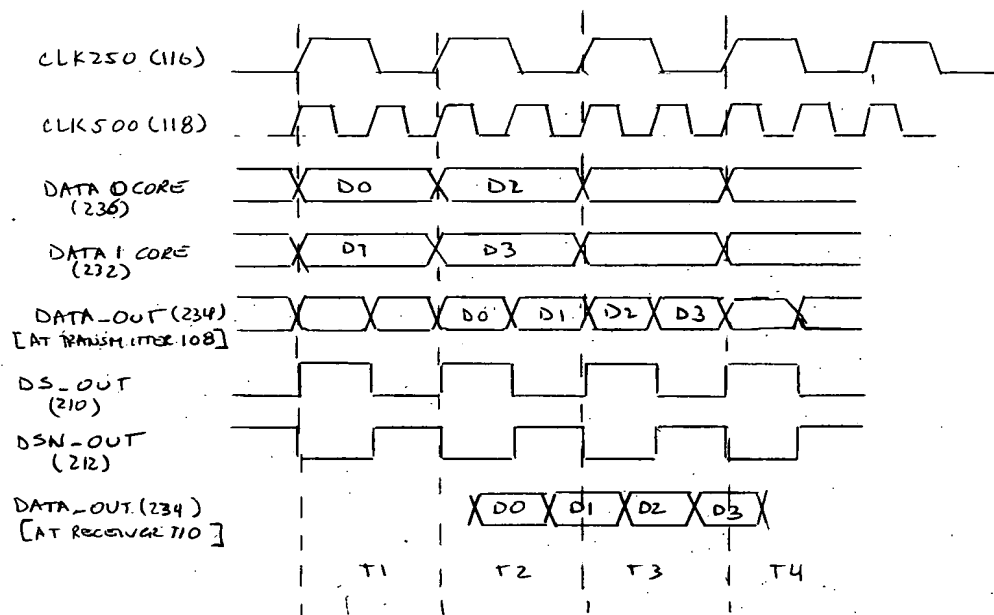
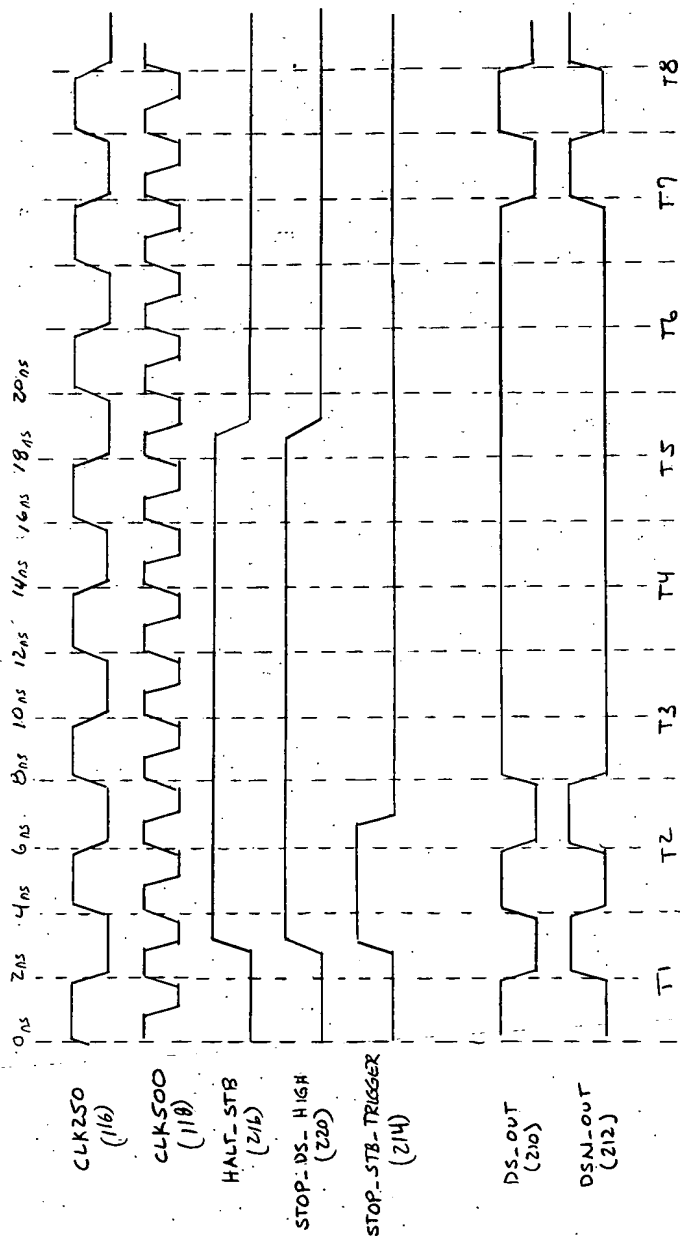


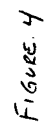
FIGURE 2B

DATA AND DATA STROBE
SIGNAL RELATIONSHIPS
200



Transmit Logic
 Input/output signal
 Relationship 300

FIGURE 3



PULSE STRETCH LOGIC 206

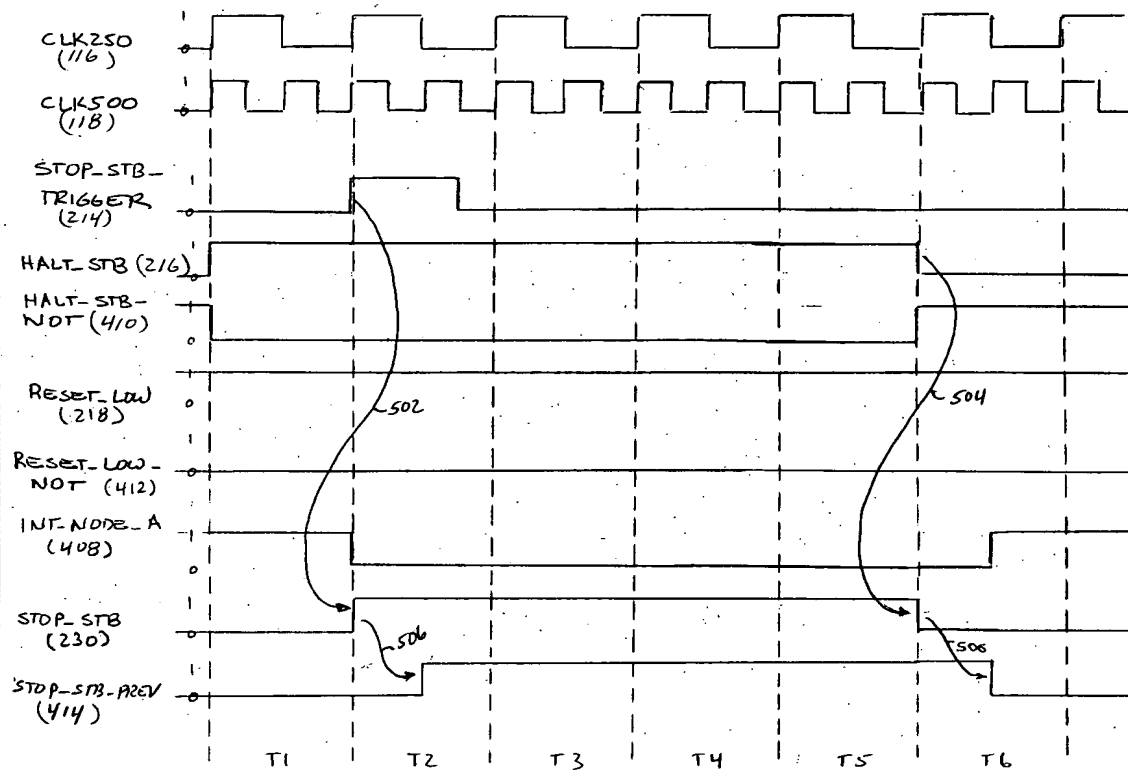
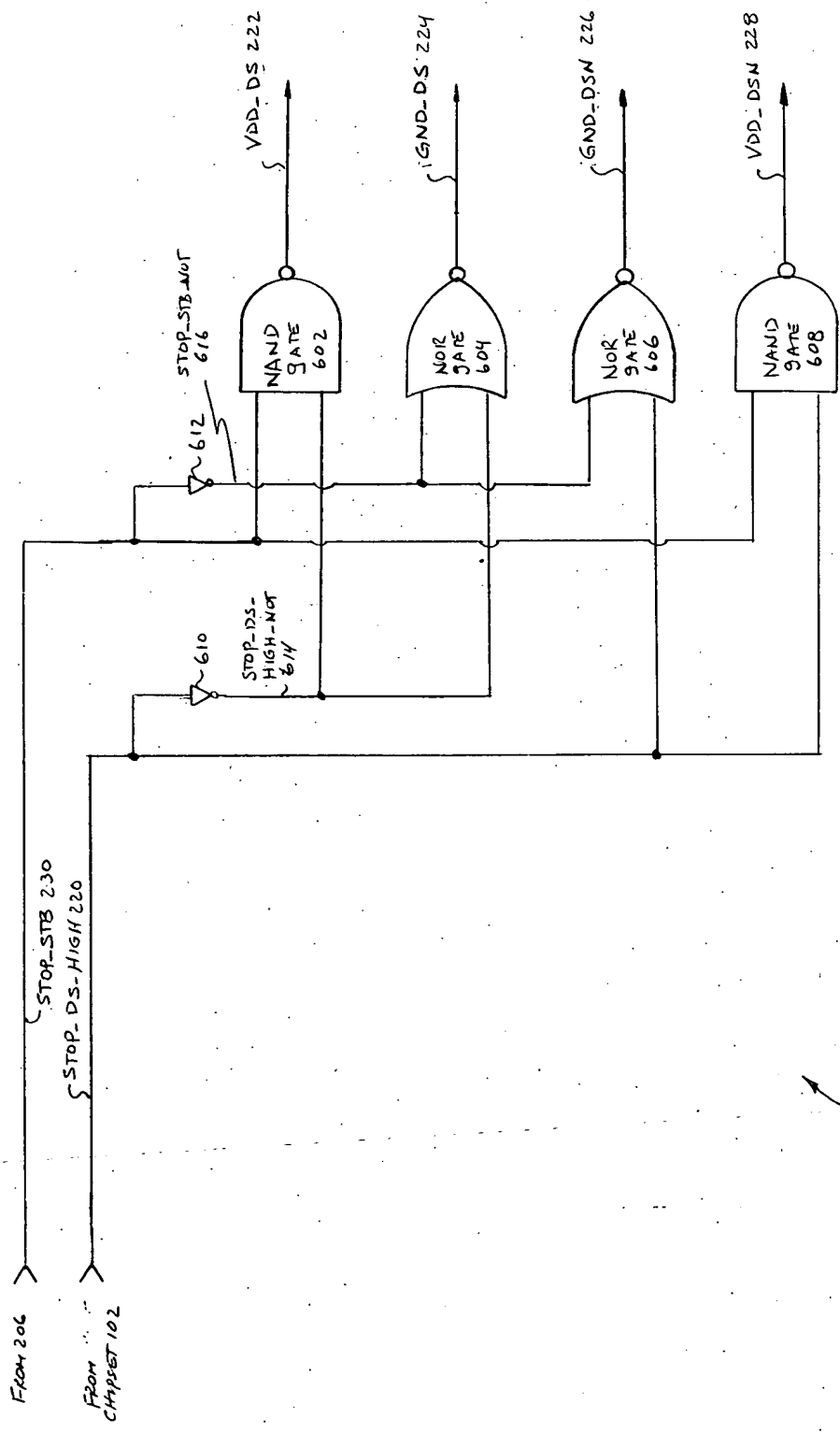


FIGURE 5

↑
PULSE STRETCH LOGIC
TIMING RELATIONSHIPS
500



DIFFERENTIAL DATA STROBE
 CONTROL LOGIC 208

FIGURE 6

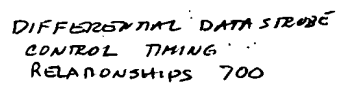


FIGURE 7

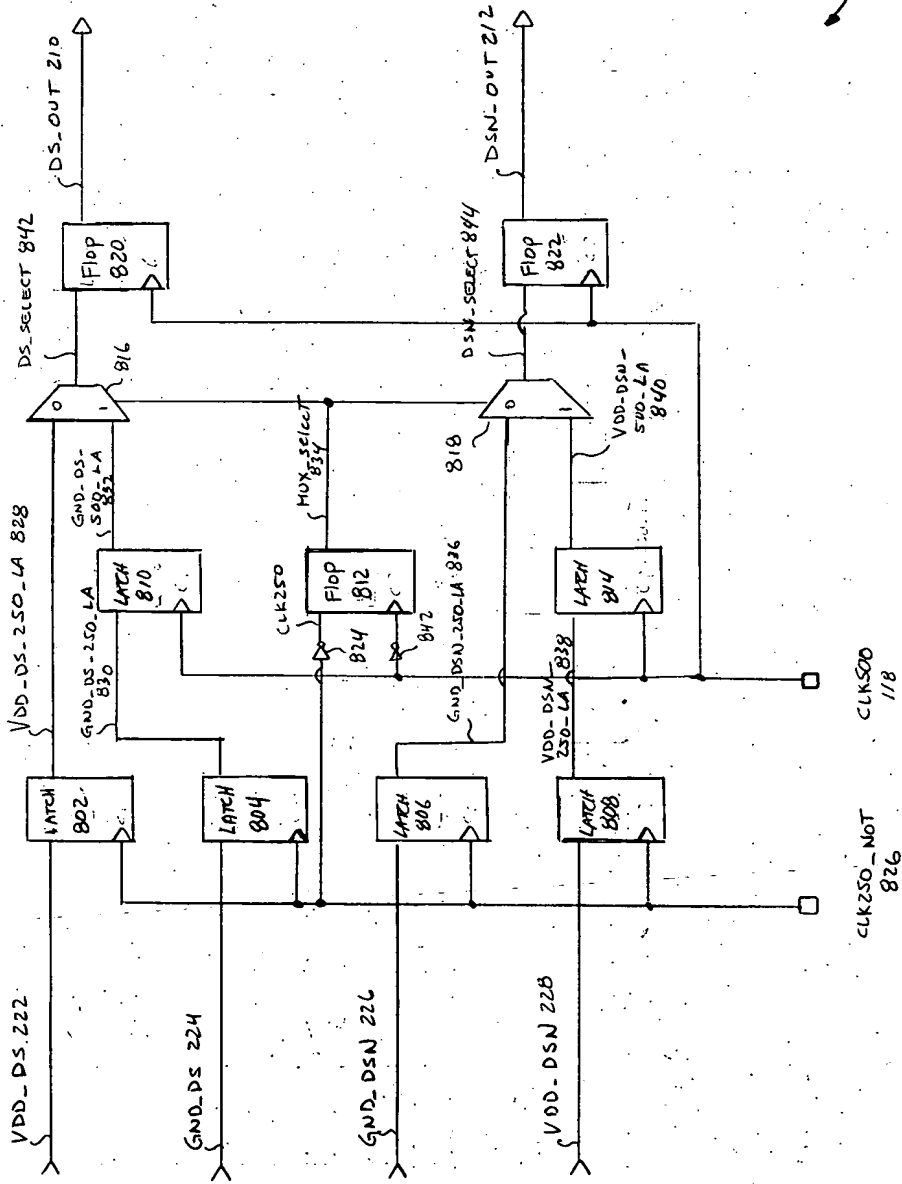


Figure 8

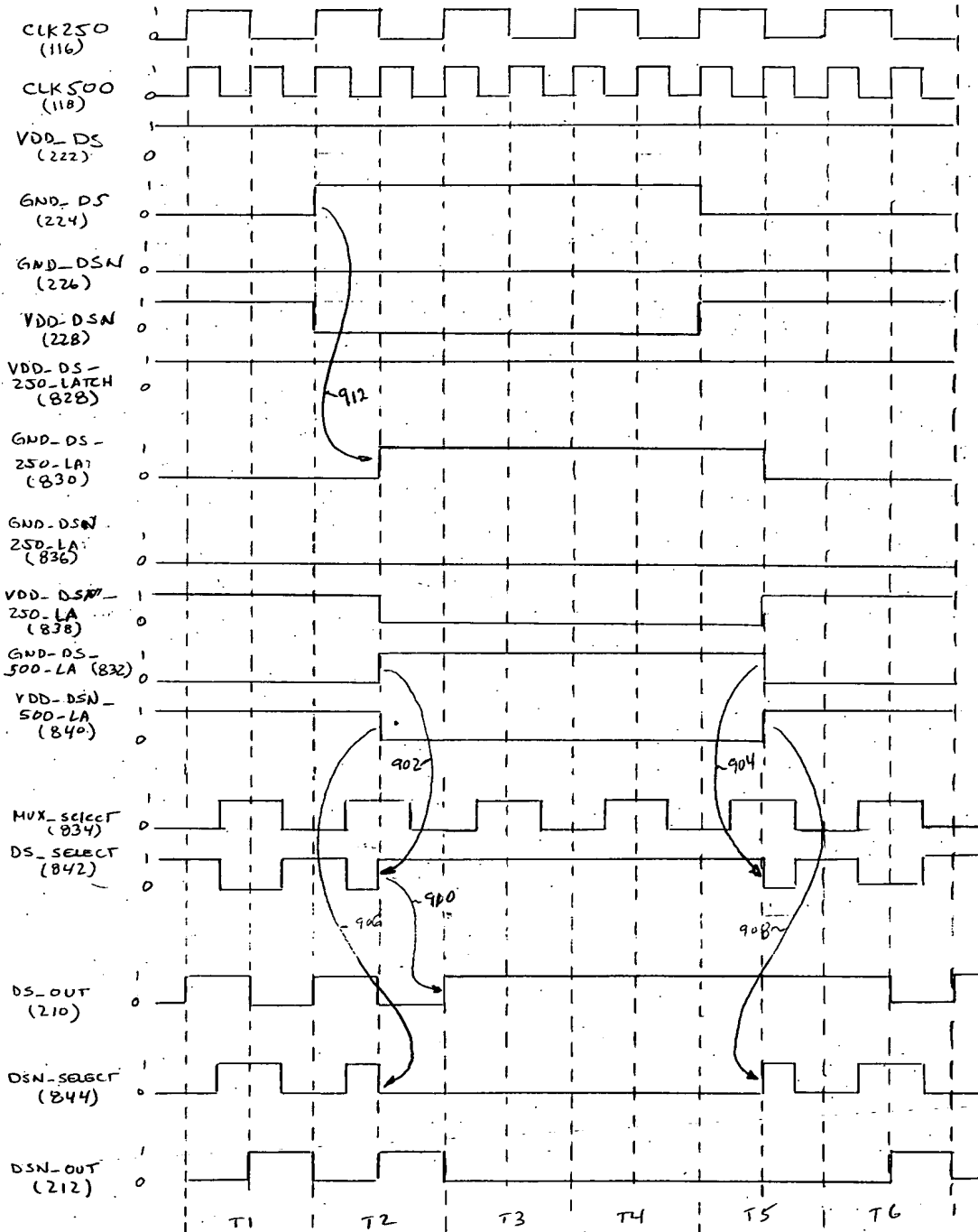


FIGURE 9

DIFFERENTIAL DATA
STROBE SIGNALS GEN.
TIMING RELATIONS PS
900

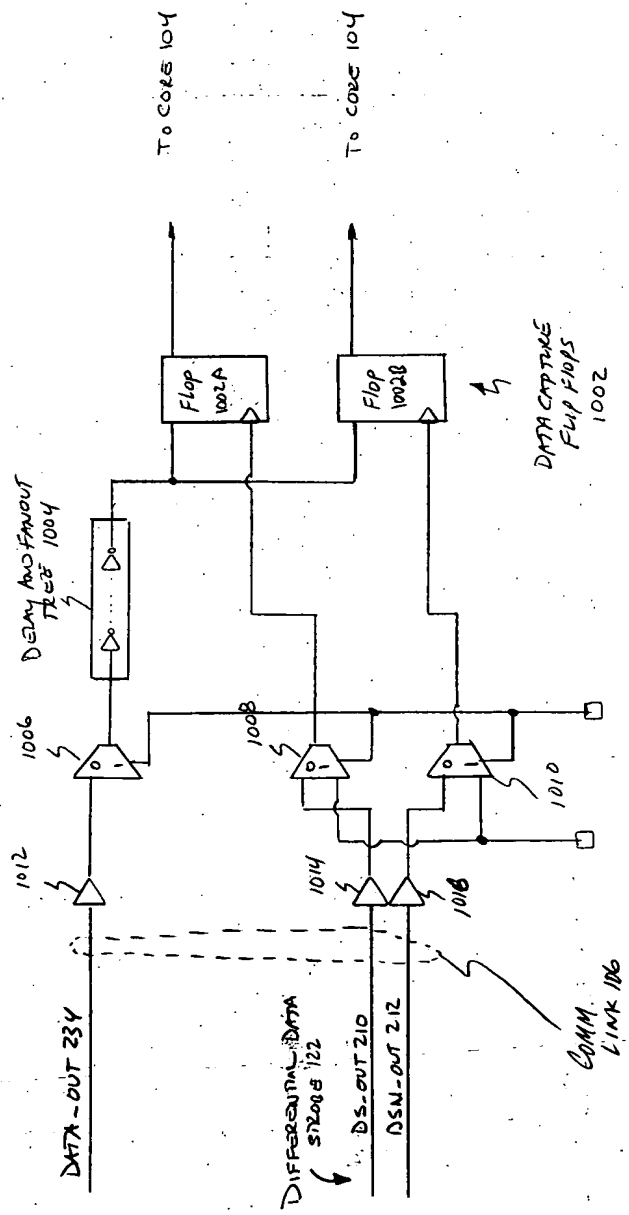
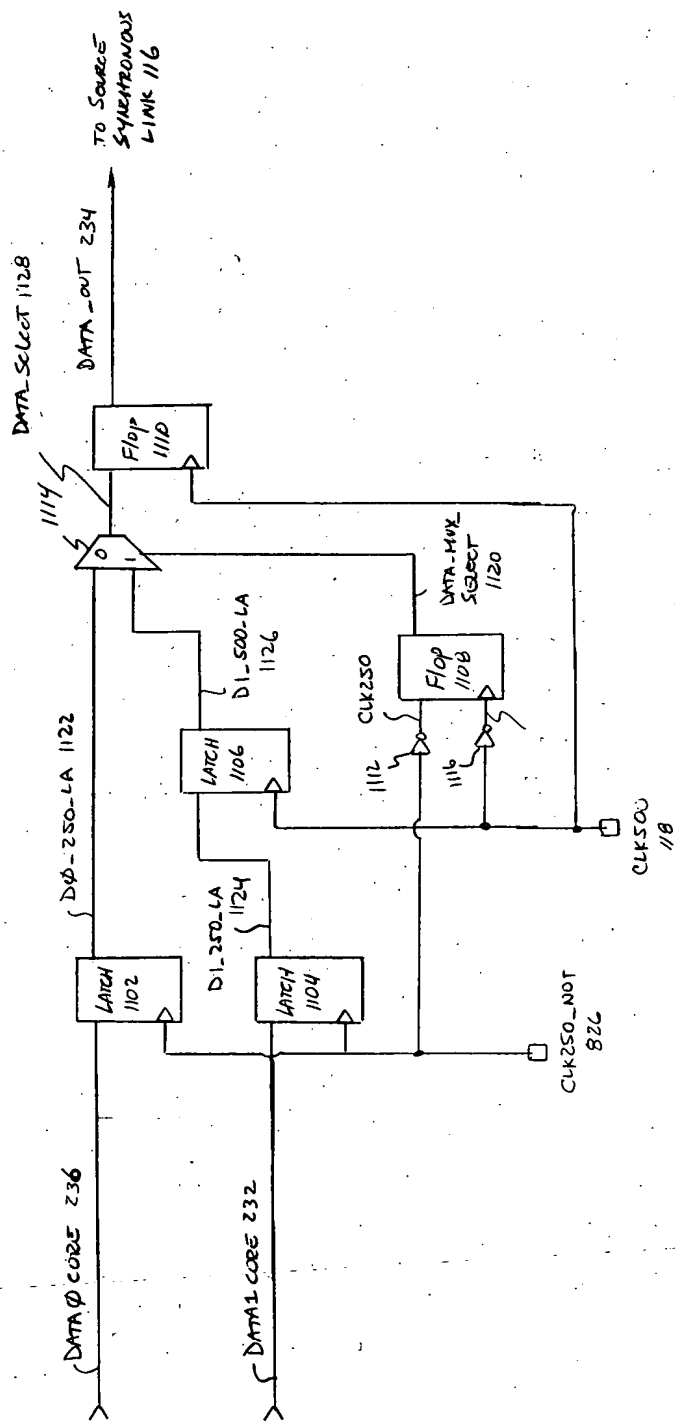


FIGURE 10

Source Synchronous Receiver 110



DATA TRANSMIT
LOGIC 112

FIGURE 11

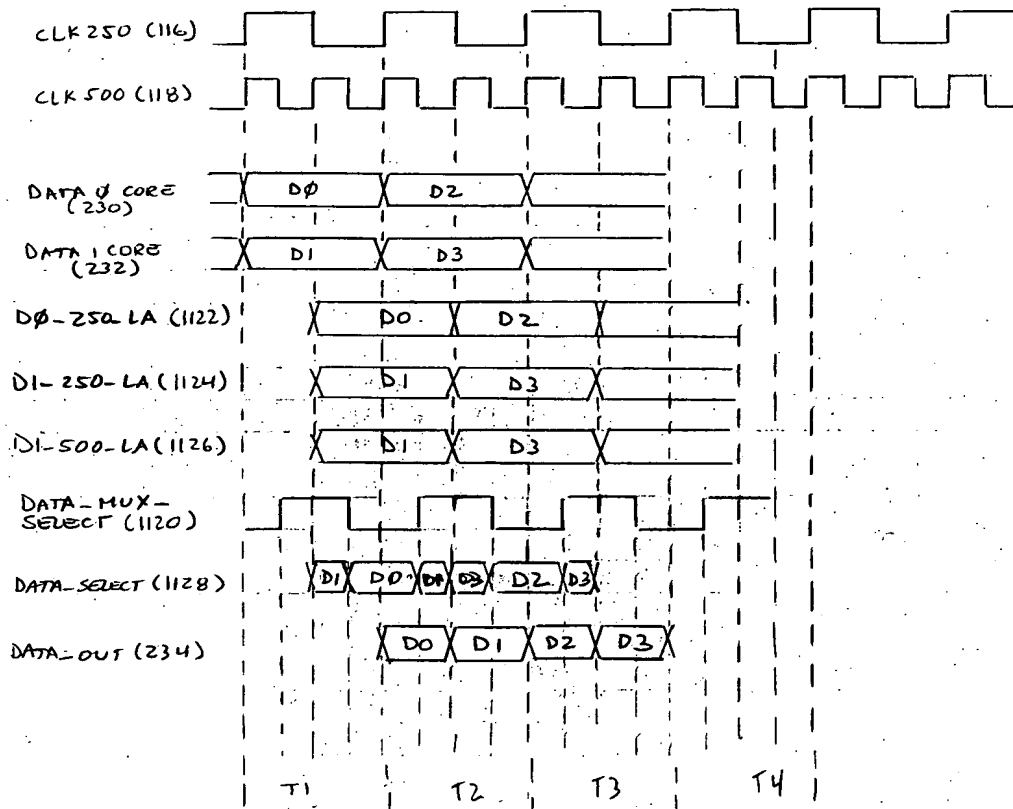


FIGURE 12

DATA TRANSMIT
SIGNAL RELATIONSHIPS
1200

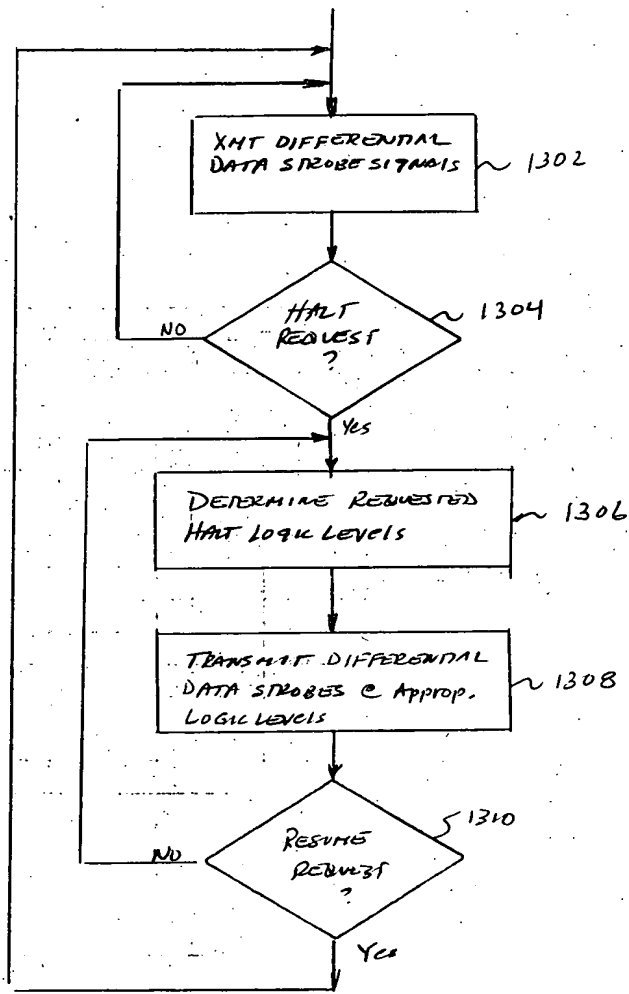


FIGURE 13